

In the Specification

Please amend page 7, line 29 - page 8, line 17 of the Specification as follows:

Digital signal processor 32 receives incoming digital signals from analog front end 30 and outgoing digital signals from serializer/deserializer 34. The signals may be processed according to conventional techniques to perform any necessary modulation, demodulation, error correction, filtering, frequency analysis and other functions associated with line card 22. The teachings of the invention are equally applicable to line cards including yet to be developed digital signal processors and associated functions. Digital signal processor 32 may also examine signals it received during testing to determine if portions of line card 22 are operating ~~property~~ properly, as described in greater detail below. Although other formats may be used, a digital multiline-based DSL is very adept at performing spectrum analyzer functions. As part of its normal operations, it performs a Fast Fourier Transform to break a received signal down into a plurality of bins. Utilizing digital signal processor 32 to examine signals during testing allows testing of portions of line card 22 without the use of expensive test equipment.